



PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Hiroyuki YANO et al.
Serial No. : 09/870,085
Filed : May 30, 2001
For : ***MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE***
Examiner : Thanhha Pham
Group Art Unit : 2813

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Sir:

Prior to continued examination of the above-identified patent application, and in response to the Final Action mailed on March 24, 2003, please amend the application as follows.